

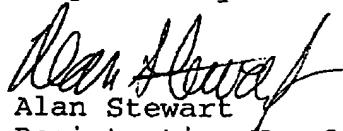
REMARKS

Claims 1-5 and 7-21 are pending in the application with claims 11-21 withdrawn from consideration. Claims 1-5 and 7-10 are rejected. Claim 6 is cancelled.

Claims 1-5 and 8-10 were rejected under 35 USC 102(b) as being anticipated by Chetlur. Claim 7 was rejected under 35 USC 103(a) as being unpatentable over Chetlur in view of Hanai. Claim 1 includes "...processing a request for a voltage overshoot or undershoot to determine a plurality of inputs based, in part, on a plurality of waveform parameters; applying the plurality of inputs to a waveform generation circuit ...". The references of record do not show, teach, or suggest the above recited limitations of claim 1. Col 3, lines 32-44 of Chetlur does not disclose determining a plurality of inputs and applying the inputs to a waveform generation circuit. Col 3, lines 32-35 of Chetlur discloses a multiplexer for selectively connecting an oscillator test signal, a first voltage, or a second voltage to the circuit path that is being tested. Col 3, lines 14-44 of Chetlur does not disclose determining a plurality of inputs based on a plurality of waveform parameters. Claims 2-5 and 7-10 depend from claim 1. Therefore, Claims 1-5 and 7-10 are believed to be allowable over the references of record.

It is believed that the above remarks and amendments are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Respectfully submitted,


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